



PATENT
Attorney Docket No. SAM-0126

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): In-Jung Lee, et al.

Examiner: Nguyen, C.

8/a

Serial No.: 09/633,366

Group Art Unit: 2811

FJG NES
6-18-02

Filing Date: August 7, 2000

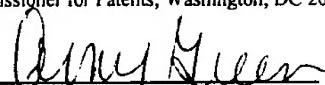
Title: CAPACITOR AND FABRICATING METHOD THEREFOR

CERTIFICATE OF MAILING UNDER 37 C.F.R. § 1.8

I hereby certify that this correspondence is being deposited with the United States Post Office as First Class Mail on the date indicated below in an envelope addressed to BOX NON-FEE AMENDMENT, Assistant Commissioner for Patents, Washington, DC 20231.

5-14-02

Date



Amy Green

BOX NON-FEE AMENDMENT

Assistant Commissioner for Patents

Washington, D.C. 20231

RECEIVED
JUN 10 2002
TECHNOLOGY CENTER 2800

AMENDMENT A

Sir:

This is in response to the Office Action mailed on February 25, 2002.

Please amend the application as follows:

In the Claims

(Amended) A semiconductor capacitor having a lower electrode, a dielectric layer and an upper electrode, wherein the upper electrode comprises a deposition structure including three layers including a doped polysilicon layer formed between a first undoped polysilicon layer and a second undoped polysilicon layer.